

EAST - [731891d:1]

File View Edit Tools Window Help

Active

- L4: (1787) connect\$4 same graph same edge
- L5: (11374) diagonal same angle ← key for RCE rejection
- L6: (397) horizontal same angle
- L7: (0) 14 and 15 and 16
- L8: (67) 14 and 15
- L9: (58) 18 and placement
- L10: (6398) wir\$3 near9 placement
- L11: (32) 18 and 110
- L12: (18) 111 and Rostoker

Failed

2/17/03

Search Browse Queue Clear

DB: USPAT-USP ☒ Plazas

Default operator: DR ☒ Highlight all hit items initially

18 and 110

ABR G4 m 1 H

	U	I	Document ID	Issue Date	Pages	Title	Current OR
10	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020100007	20020725		Recursive partitioning placement method and	716/7
11	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020073390	20020613		Method and apparatus for using a diagonal li	716/8
12	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020069397	20020606		Method and apparatus for placing circuit mod	716/12
13	<input type="checkbox"/>	<input type="checkbox"/>	US 6678872 B2	20040113	44	Method and apparatus for using a diagonal li	716/7
14	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6671864 B2	20031230	44	Method and apparatus for using a diagonal li	716/8
15	<input type="checkbox"/>	<input type="checkbox"/>	US 6651233 B2	20031118	74	Method and apparatus for measuring congestio	716/7
16	<input type="checkbox"/>	<input type="checkbox"/>	US 6516455 B1	20030204	48	Partitioning placement method using diagonal	716/7
17	<input type="checkbox"/>	<input type="checkbox"/>	US 6407434 B1	20020618	136	Hexagonal architecture	257/401
18	<input type="checkbox"/>	<input type="checkbox"/>	US 6312980 B1	20011106	134	Programmable triangular shaped device having	438/197
19	<input type="checkbox"/>	<input type="checkbox"/>	US 6301686 B1	20011009	51	Graphic layout compaction system capable of	716/2
20	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6097073 A	20000801	138	Triangular semiconductor or gate	257/401
21	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 5973376 A	19991026	136	Architecture having diamond shaped or parall	257/401
22	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5889329 A	19990330	139	Tri-directional interconnect architecture fo	257/758
23	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5872380 A	19990216	136	Hexagonal sense cell architecture	257/369
24	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5864165 A	19990126	141	Triangular semiconductor NAND gate	257/401
25	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5834821 A	19981110	140	Triangular semiconductor "AND" gate device	257/401